

		LKS06x	LKS08x	LKS05x	LKS45x	LKS03x	LKS07x	
	Freq/MHz	96	96	96	192	48	96	
	Processor	32bit RISC	Cortex-M0	Cortex-M0	32bit RISC with FPU/DSP	32bit RISC	32bit RISC	
	DSP	DIV/SQRT peripherals ¹	Coprocessor ² with DIV/SQRT/CORDIC ability	SQRT/CORDIC peripherals	CORDIC peripherals	DIV/SQRT peripherals	Coprocessor with DIV/SQRT/CORDIC ability	
	FMAC(Filter Accelerator)				√			
	DMA Channel/#		4		8	4	4	
	Debug	SWD	SWD	SWD	SWD/JTAG	SWD	SWD	
Storage	Flash/kB	Main	32	64/32	32	256/128	32/16	128/64
		NVR	0	1	1	18		2
	ROM/kB	-	-	-		16		
	SRAM/kB	4	8	2.5	40	4	12	
Analog	ADC	#	12bit×2	12bit×1	12bit×1	14bit×3	12bit×1	12bit×2
		Sampling rate/Msps	3	3	3	2	1	3
	DAC	#	12bit×1	12bit×1	12bit×1	12bit×1	8bit×1	12bit×2
		Range/V	1/3.3	1.2/3/4.8	1.2/4.8	1.2/3.3	3.0	1.2/4.85
	OPA/#	4	4	2/4	6	1/2	4	
	CMP/#	2	2	2	6	2	3	
GPIO	#(max)	48	64	48	84	25	64	
	Pull-up	√	√(Some are available)	√(Some are available)	√	√(Some are available)	√(Some are available)	
	Pull-down	√			√			
	Open-drain		√	√	√	√	√	
	SWD as GPIO			√	√	√	√	
	RSTn as GPIO		√	√	√	√	√	
Peripherals	UART	2	2	2	3	1	2+1(DSP emulated)	
	IIC	1	1	1	2	1	1	
	SPI	1	1	1	2	1	1	
	TIMER	16bit×4	16bit×2 + 32bit×2	16bit×2 + 32bit×2	16bit×3 + 32bit×2	16bit×1 + 32bit×1	16bit×2 + 32bit×2	
	QEP	2	2		4		2	
	CAN		√		√(CAN-FD)		√	
	SIF		√				√	
	Power Management	SLEEP/DEEPSLEEP	SLEEP/DEEPSLEEP	SLEEP/DEEPSLEEP	SLEEP/DEEPSLEEP/ Power Gating	SLEEP/DEEPSLEEP	SLEEP/DEEPSLEEP	
	WatchDog	1	1	1	2(IWDG+WWDG)	1	1	
Clocks	HRC/MHz	4	4	4	12	4	8	
	LRC/kHz	32	32	64	64	64	64	
	XTAL/MHz	4	4/8		12/24		8	
	PLL/MHz	96	96	96	192	48	96	